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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>  <b>Mail Stop: AMENDMENT</b>  Express Mail Receipt No.  Total Number of Pages in This Submission	Application / Conf. No.	10/612,522 / 5307
	Filing Date	July 02, 2003
	First Named Inventor	Cameron D. Patterson
	Examiner Name	Stephen C. Elmore
	Art Unit	2186
	Patent No.	
Attorney Docket Number		X-1405 US

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input checked="" type="checkbox"/> Amendment / Reply <input type="checkbox"/> Preliminary Amendment <input type="checkbox"/> After Final <input type="checkbox"/> Affidavit(s)/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Change Status to LARGE ENTITY <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A) <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet) <input type="checkbox"/> Declaration / Oath <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition -  <input type="checkbox"/> To Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund	<input type="checkbox"/> After Allowance Communication to <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Return Receipt Postcard <input type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; height: 40px; width: 100%;"></div>
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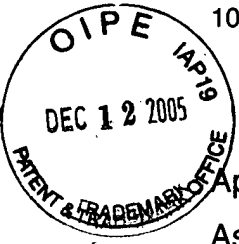
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X-1405 US  
10/612,522

PATENT  
Conf. No. 5307



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cameron D. Patterson

Assignee: Xilinx, Inc.

Title: Field Programmable Gate Array (FPGA) Configuration Data Path for  
Module Communication

Serial No.: 10/612,522

Filing Date: 07/02/2003

Examiner: Stephen C. Elmore

Art Unit: 2186

Docket No.: X-1405 US

Conf. No.: 5307

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Mail Stop Amendment  
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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on  
September 14, 2005, Applicant submits the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 6 of this paper.